

Lecture 1

From ece720 webpage: Review syllabus, and read chp. 1 of the Razavi Analog book. Also look at the Op Amp Performance – High Level Modeling link

Online analog books and content

www.e-dsp.com

Designing Analog Chips by Hans Camenzind - read intro and MOS transistor modeling section (Under Simulation). His website is www.arraydesign.com

<http://www.rfzone.org/free-rf-ebooks/>

Op amps for Everyone book at both e-dsp and rfzone

Look at the classroom spice models and the bsim 3.3 models for the ami05 process on the mosis web page:

http://www.mosis.org/Technical/Testdata/t15d_ami_c5n_level3.txt

<http://www.mosis.org/cgi-bin/cgiwrap/umosis/swp/params/ami-c5/t69k-params.txt>

Circuits 1 course at MIT Open CourseWare:

In general, look at the Lecture Notes and Videos. Specifically, look at inside the digital gate for simple MOS transistor model, and lectures 6-11 for small signal vs. large signal modeling.

<http://ocw.mit.edu/OcwWeb/Electrical-Engineering-and-Computer-Science/6-002Spring-2007/CourseHome/index.htm>

TI design contest

www.ti.com/analoguniversityprogram