Lecture 1

From ece720 webpage:    Review syllabus, and read chp. 1 of the Razavi Analog book.
Also look at the Sheets:  Mixed Signal System, System Decomposition…., Op Amp
Performance – High Level Modeling link, and Art of Modeling Issue.

Online analog books and content

720 Lecture Notes:  Review key Sedra/Smith pages and some of the online slides.

Designing Analog Chips by Hans Camenzind   -   read intro and MOS transistor
modeling section (Under Simulation).    His website is www.arraydesign.com

Look at the classroom spice models and the bsim 3.3 models for the ami05 process on the
mosis web page:

http://www.mosis.com/files/test_data/t15d_ami_c5n_level3.txt

http://www.mosis.org/cgi-bin/cgiwrap/umosis/swp/params/ami-c5/t69k-params.txt

Circuits 1 course at MIT Open CourseWare:

In general, look at the Lecture Notes and Videos.  Specifically, look at inside the digital
gate for simple MOS transistor model, and lectures 6-11 for small signal vs. large signal
modeling.

http://ocw.mit.edu/OcwWeb/Electrical-Engineering-and-Computer-Science/6-002Spring-
2007/CourseHome/index.htm

TI design contest

www.ti.com/analoguniversityprogram

Overview MOS transistor theory using the Lecture slides from the ece721 open website:
Book links – Author's course website:   Slides on Transistor Theory and Non-Ideal
Transistors.